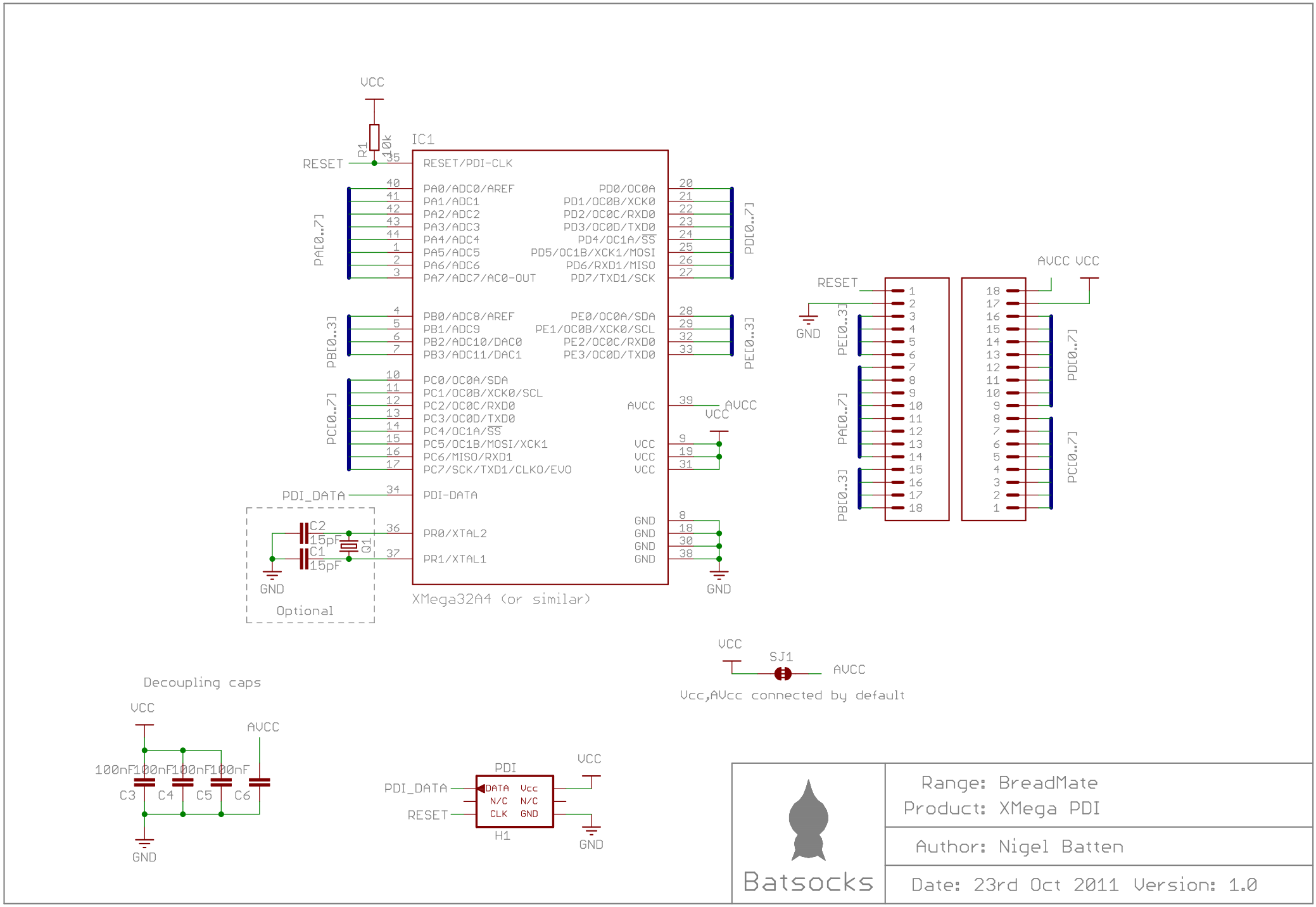
VCC
R1
10k
IC1
RESET
35
RESET/PDI-CLK
PA[0..7]
40 PA0/ADC0/AREF
41 PA1/ADC1
42 PA2/ADC2
43 PA3/ADC3
44 PA4/ADC4
1 PA5/ADC5
2 PA6/ADC6
3 PA7/ADC7/AC0-OUT
PD[0..7]
20 PD0/OC0A
21 PD1/OC0B/XCK0
22 PD2/OC0C/RXD0
23 PD3/OC0D/TXD0
24 PD4/OC1A/SS
25 PD5/OC1B/XCK1/MOSI
26 PD6/RXD1/MISO
27 PD7/TXD1/SCK
PB[0..3]
4 PB0/ADC8/AREF
5 PB1/ADC9
6 PB2/ADC10/DAC0
7 PB3/ADC11/DAC1
PE[0..3]
28 PE0/OC0A/SDA
29 PE1/OC0B/XCK0/SCL
32 PE2/OC0C/RXD0
33 PE3/OC0D/TXD0
PC[0..7]
10 PC0/OC0A/SDA
11 PC1/OC0B/XCK0/SCL
12 PC2/OC0C/RXD0
13 PC3/OC0D/TXD0
14 PC4/OC1A/SS
15 PC5/OC1B/MOSI/XCK1
16 PC6/MISO/RXD1
17 PC7/SCK/TXD1/CLKO/EVO
AVCC
39
VCC
9
19
31
PDI_DATA
34
PDI-DATA
C2
15pF
C1
15pF
Q1
36 PR0/XTAL2
37 PR1/XTAL1
GND
8
18
30
38
GND
Optional
XMega32A4 (or similar)
RESET
GND
PE[0..3]
PA[0..7]
PB[0..3]
AVCC VCC
PD[0..7]
PC[0..7]
VCC
SJ1
AVCC
Vcc,AVcc connected by default
Decoupling caps
VCC
AVCC
100nF 100nF 100nF 100nF
C3 C4 C5 C6
GND
PDI
VCC
PDI_DATA
RESET
DATA Vcc
N/C N/C
CLK GND
H1
GND
Batsocks
Range: BreadMate
Product: XMega PDI
Author: Nigel Batten
Date: 23rd Oct 2011 Version: 1.0